

**TRANSMITTAL**

Electronic Version v1.1

Stylesheet Version v1.1.0

**Title of  
Invention****STRUCTURE AND METHOD OF MAKING A BIPOLAR TRANSISTOR  
HAVING REDUCED COLLECTOR-BASED CAPACITANCE**

Application Number :

Date :

First Named Applicant: Hiroyuki Akatsu

Confirmation Number:

Attorney Docket Number: FIS920030415US1

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Submitted By:

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Sign. Capacity

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Agent

Documents being submitted:	Files
us-ids	FIS920030415US1-usidst.xml us-ids.dtd us-ids.xsl
us-request	FIS920030415US1-usrequ.xml us-request.dtd us-request.xsl
us-assignment	FIS920030415US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030415US1Assignment1.tif FIS920030415US1Assignment2.tif FIS920030415US1Assignment3.tif
us-fee-sheet	FIS920030415US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS920030415US1Declaration3.tif
us-declaration	FIS920030415US1Declaration4.tif
application-body	FIS920030415US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd FIS920030415US1Formula.tif FIS920030415US1Drawing1.tif FIS920030415US1Drawing2.tif FIS920030415US1Drawing3.tif

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	FIS920030415US1Drawing6.tif
	FIS920030415US1Drawing7.tif
	FIS920030415US1Drawing8.tif
us-declaration	FIS920030415US1Declaration1.tif
us-declaration	FIS920030415US1Declaration2.tif
<b>Comments</b>	
Assignee: International Business Machines Corporation, Armonk, New York	